

ABSTRACT OF THE DISCLOSURE

A photoelectric leak current compensating circuit of the present invention has such an arrangement as to include: a third PNP transistor, whose emitter is connected to a collector of the first PNP transistor of a Darlington circuit and whose collector is grounded; and a fourth NPN transistor whose collector is connected to a base of the third PNP transistor and whose base and emitter are connected to each other. Because a photoelectric leak current of the Darlington circuit is compensated by using a collector current, the photoelectric leak current can be compensated with a high accuracy. Moreover, because the compensating circuit including the third PNP transistor and the fourth NPN transistor, whose N-type epitaxial layers have substantially identical areas as those of transistors of the Darlington circuit, is so provided that the compensating circuit and the Darlington circuit are equally influenced by light, it is possible to prevent an increase of a chip area of the compensating circuit.